

PATENT NUMBER

**O.I.P.E.**

**PATENT DATE**

FA  
SCANNED TR3 Q.A. 7W

## APPLICANTS

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**TITLE**

Substrate layout method and structure for reducing cross talk of adjacent signals

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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